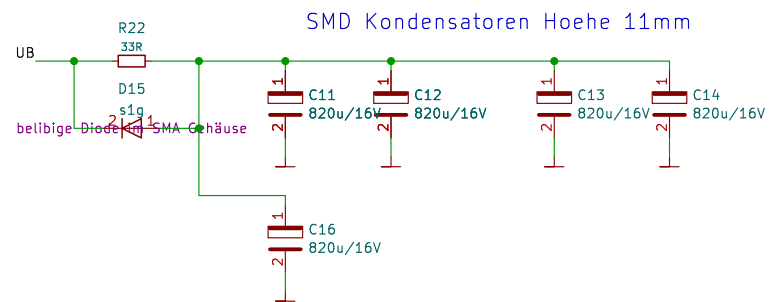
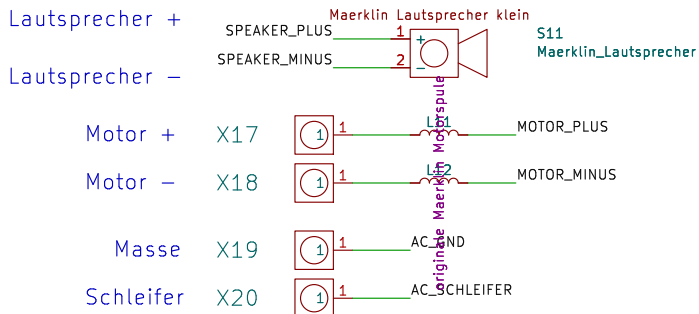
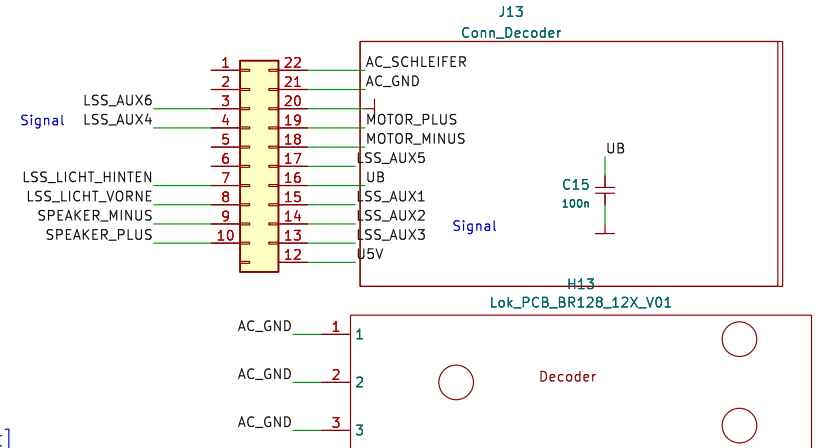
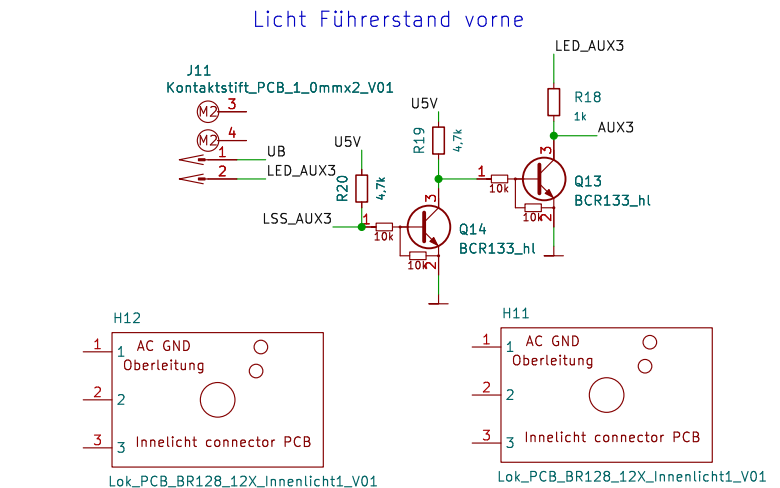
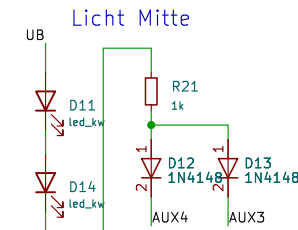
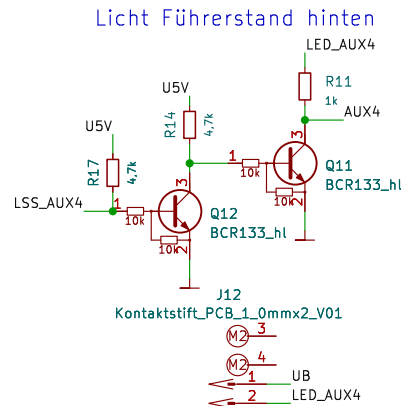




- 1 Hallsensor1
- 2 Hallsensor2
- 3 Aux6 (L)
- 4 Aux4 (L)
- 5 Zugbus Clock
- 6 zugbus Data
- 7 Licht hinten [gelb]
- 8 Licht vorne [grau]
- 9 Lautsprecher (-)
- 10 Lautsprecher (+)
- Verpolschutz



Maerklin_BR128_X12_V01

Decoder Traeger und Puffer PCB fuer BR103

Sheet: /			
File: Maerklin_BR128_X12_V01.kicad_sch			
Title: Maerklin_BR128_X12_V01			
Size: A4	Date: 22.06.2025	hlmade!	Rev:
KiCad E.D.A. 9.0.3			Id: 1/1